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INFORMATION DISCLOSURE

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: K. NISHIOKA et al. : 24 January 2002

Group Art Unit : 2783 (Anticipated)

Examiner : J. Follansbee (Anticipated)

U.S. PATENT DOCUMENTS

Examiner	Patent	Patent		Class/ Filing
<u>Initial</u>	<u>Number</u>	<u>Date</u>	<u>Name</u>	Subclass Date
VV.	5,530,817	06/25/96	MASUBUCHI	395
VN,	5,057,837	10/15/91	COLWELL et al.	341/55
$\overline{V} N$	5,870,618	02/1999	FUJIKAWA et al.	395/800.01
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	5,513,363	04/1996	KUMAR et al.	395/800
VIV	5,301,340	04/1994	COOK	395/800
VIV	5,101,489	03/1992	SATO et al.	395/425
VN	5,437,043	07/1995	FUJII et al.	395/800

FOREIGN PATENT DOCUMENTS

Examiner	Document			Class/	Translation
<u>Initial</u>	<u>Number</u>	<u>Date</u>	Country	<u>Subclass</u>	Yes No
_VN	63-098733	04/30/88	Japan		ABS.
VN	05-143333	06/11/93	Japan		ABS.
VN	04-308930	10/30/92	Japan		ABS.
VN.	05-233281	09/10/93	Japan		ABS.
VN	01-133138	05/25/89	Japan		ABS.

OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner Initial

1. MICROPROCESSOR REPORT, Philips Hopes to Displace DSPs with VLIW, Brian Case, Dec. 5, 1994, pp. 12-15.

VN

MICROPROCESSOR REPORT, UltraSpace Adds Multimedia Instructions, Linley Gwennap, Dec. 5, 1994, pp. 16-18.

EXAMINER

VAN H. NGUYEN

2.

DATE CONSIDERED
9/18/04

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.